FIG. 1

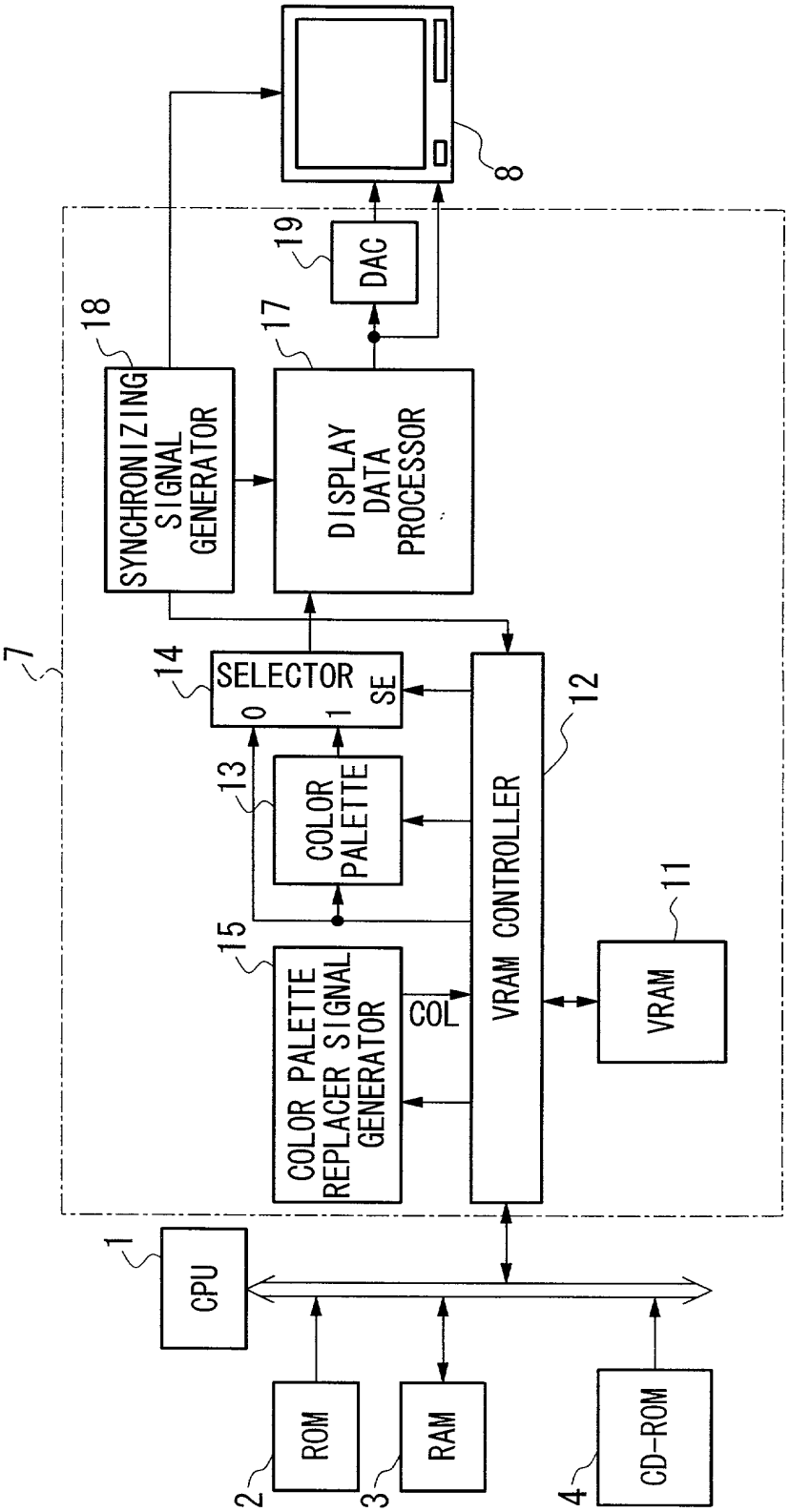

8
19
DAC
18
SYNCHRONIZING SIGNAL GENERATOR
17
DISPLAY DATA PROCESSOR
7
14
SELECTOR
0
1
SE
12
13
COLOR PALETTE
VRAM CONTROLLER
11
15
COLOR PALETTE REPLACER SIGNAL GENERATOR
COL
VRAM
1
CPU
2
ROM
3
RAM
4
CD-ROM

FIG. 2

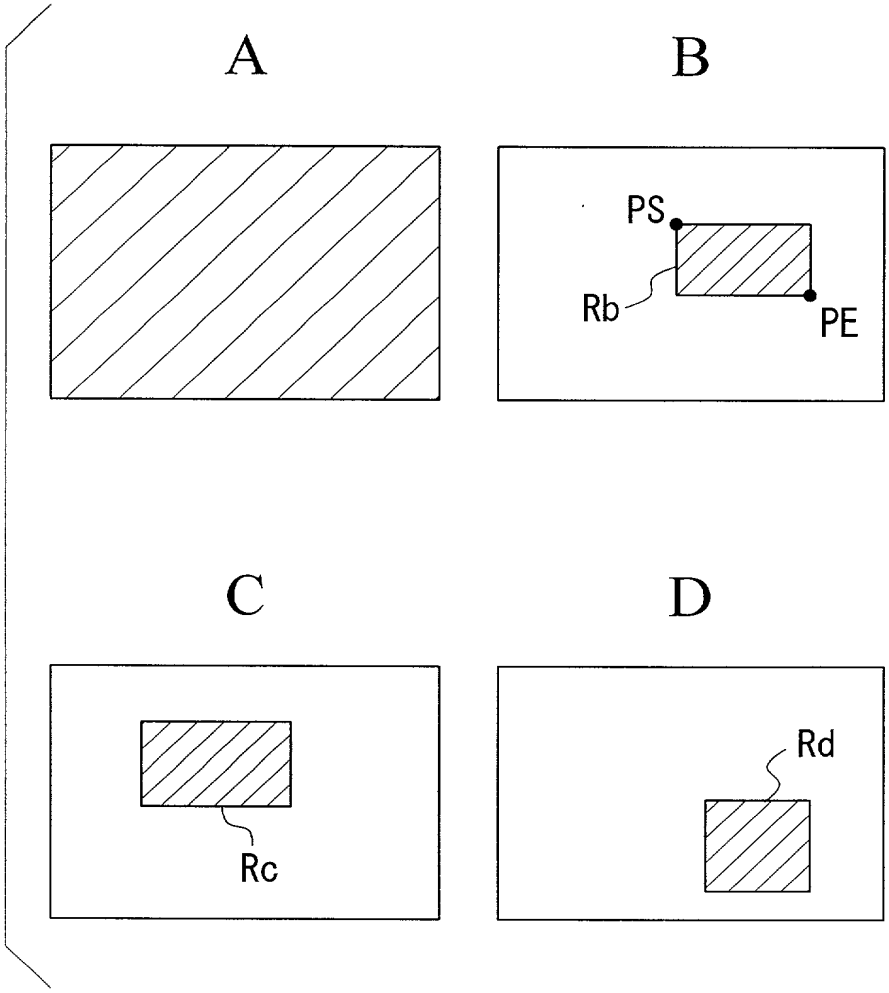


FIG. 3

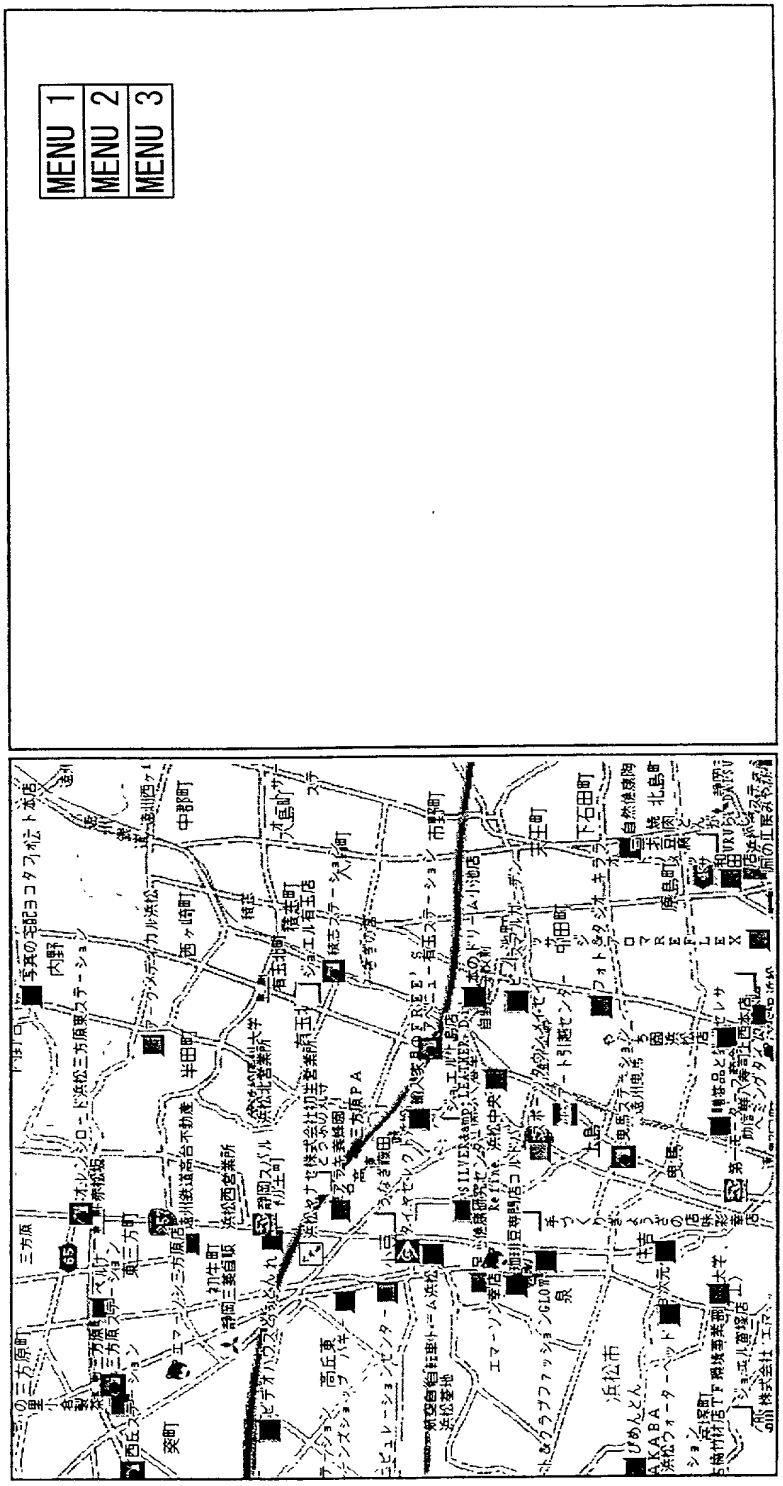


FIG. 4

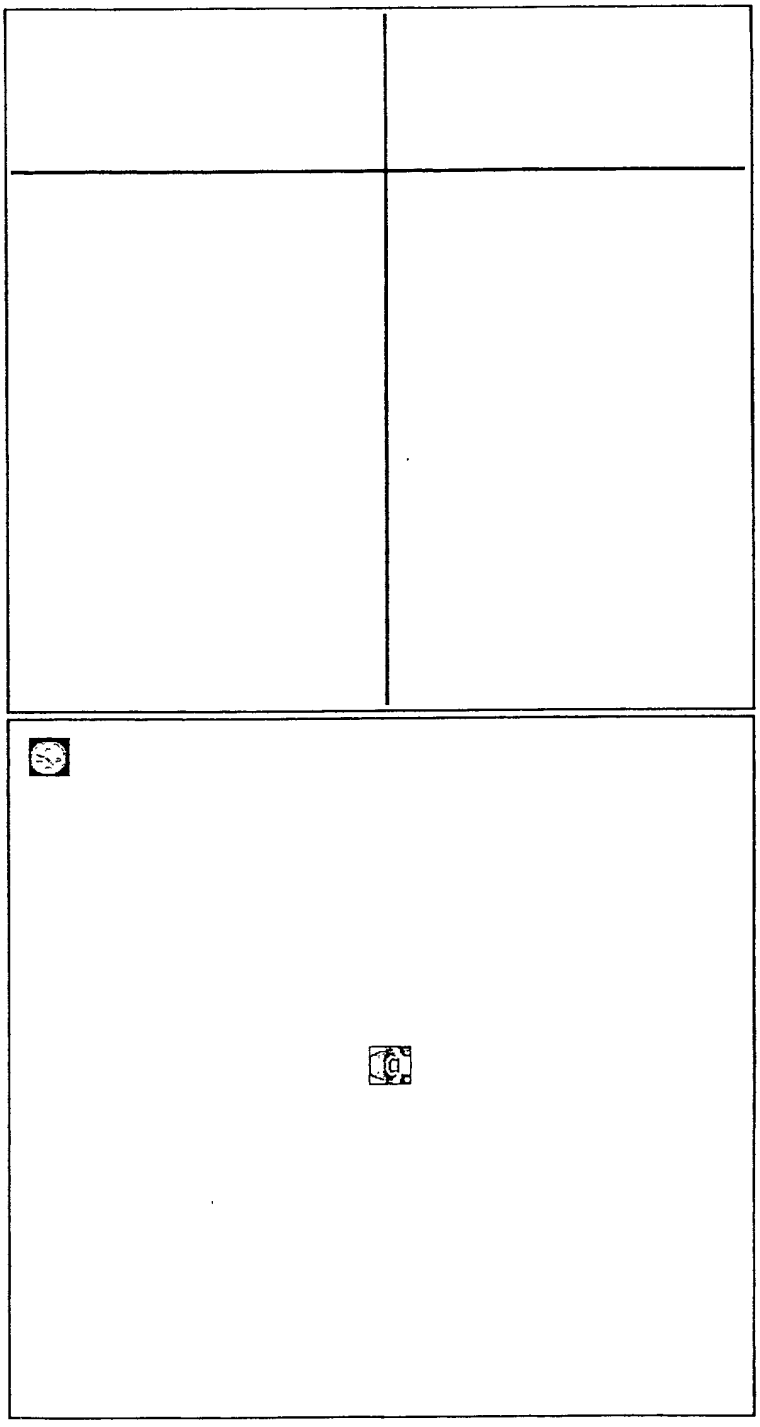


FIG. 5

DISPLAY SCREEN

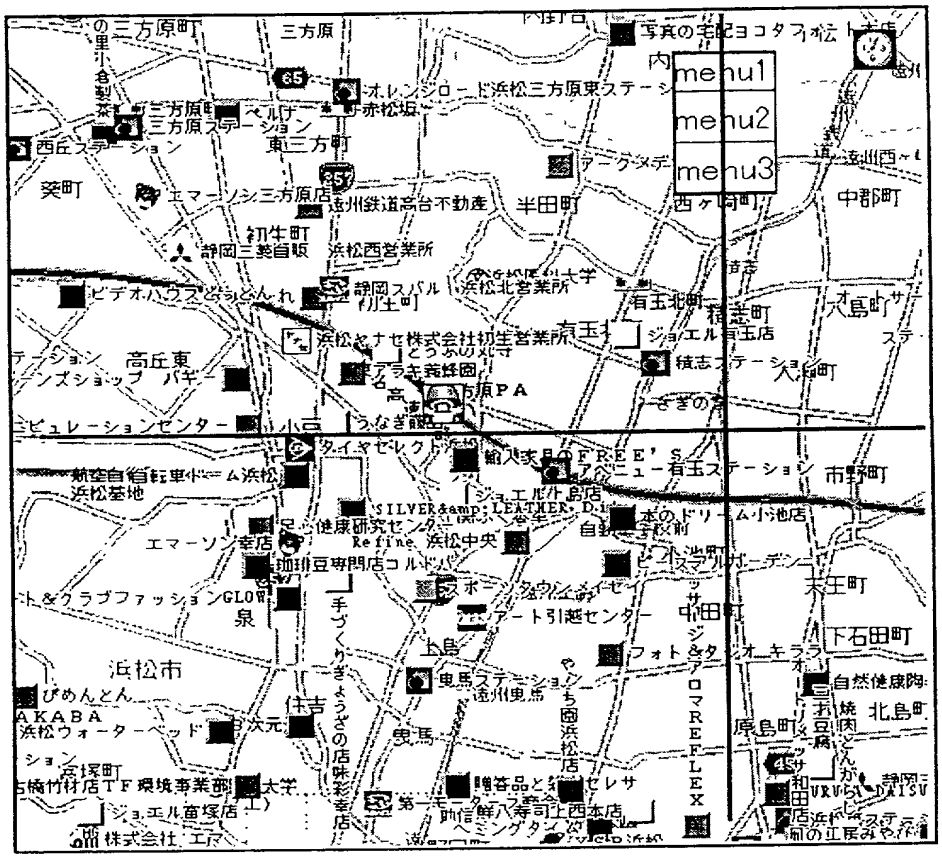


FIG. 6

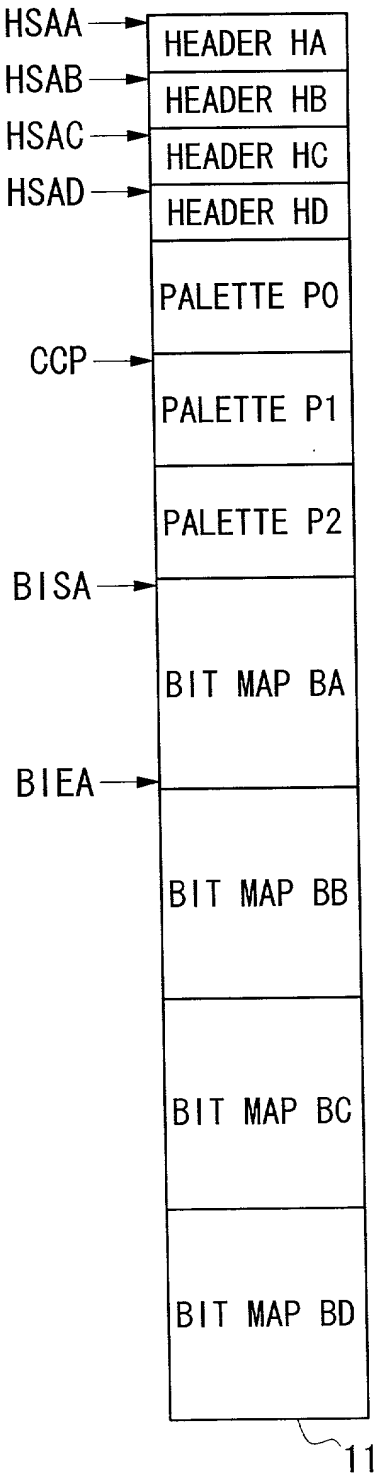

HSAA
HSAB
HSAC
HSAD
HEADER HA
HEADER HB
HEADER HC
HEADER HD
PALETTE P0
CCP
PALETTE P1
PALETTE P2
BISA
BIT MAP BA
BIEA
BIT MAP BB
BIT MAP BC
BIT MAP BD
11

FIG. 7

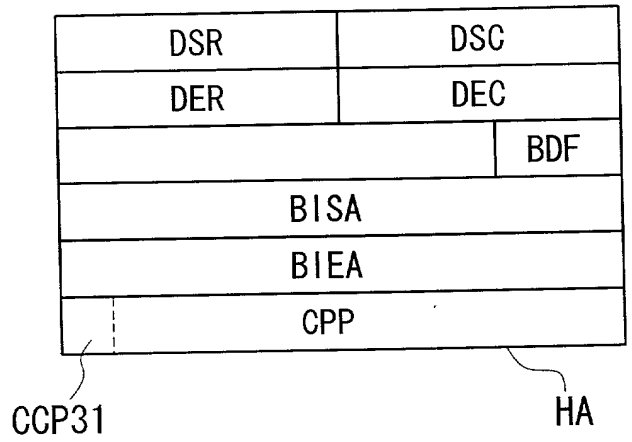

DSR
DSC
DER
DEC
BDF
BISA
BIEA
CPP
CCP31
HA

FIG. 8

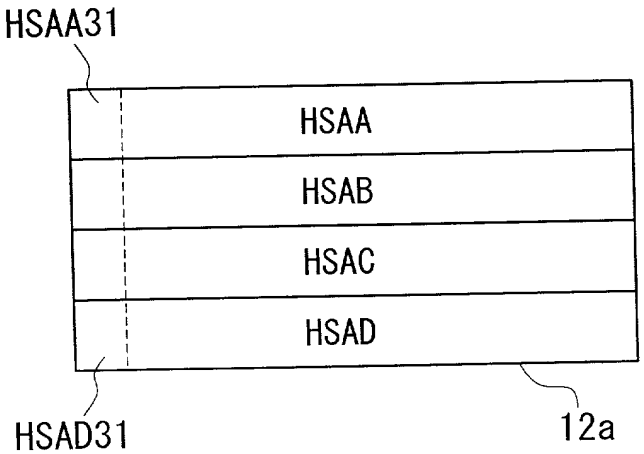

HSAA31
HSAA
HSAB
HSAC
HSAD
HSAD31
12a

FIG. 9

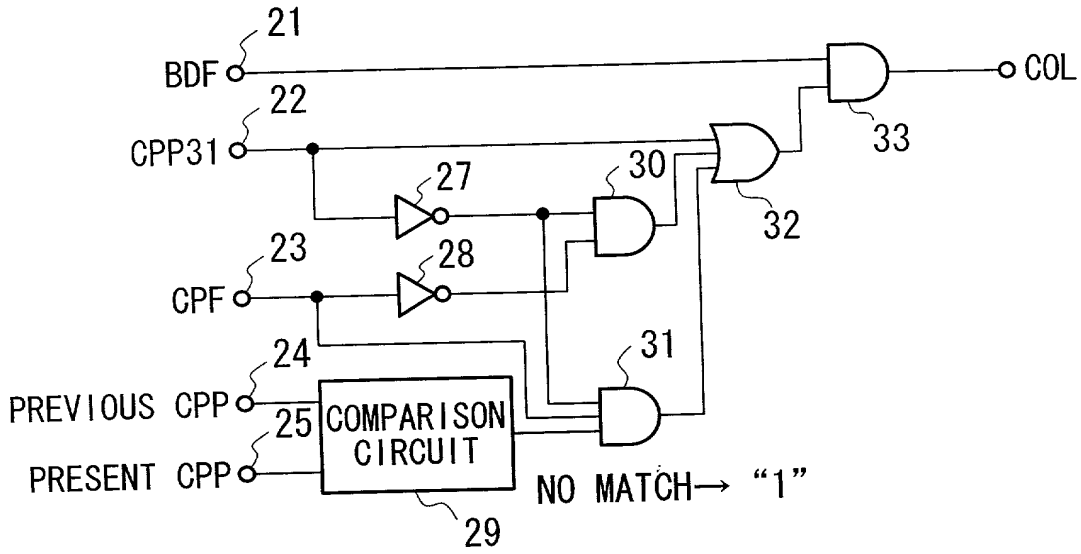
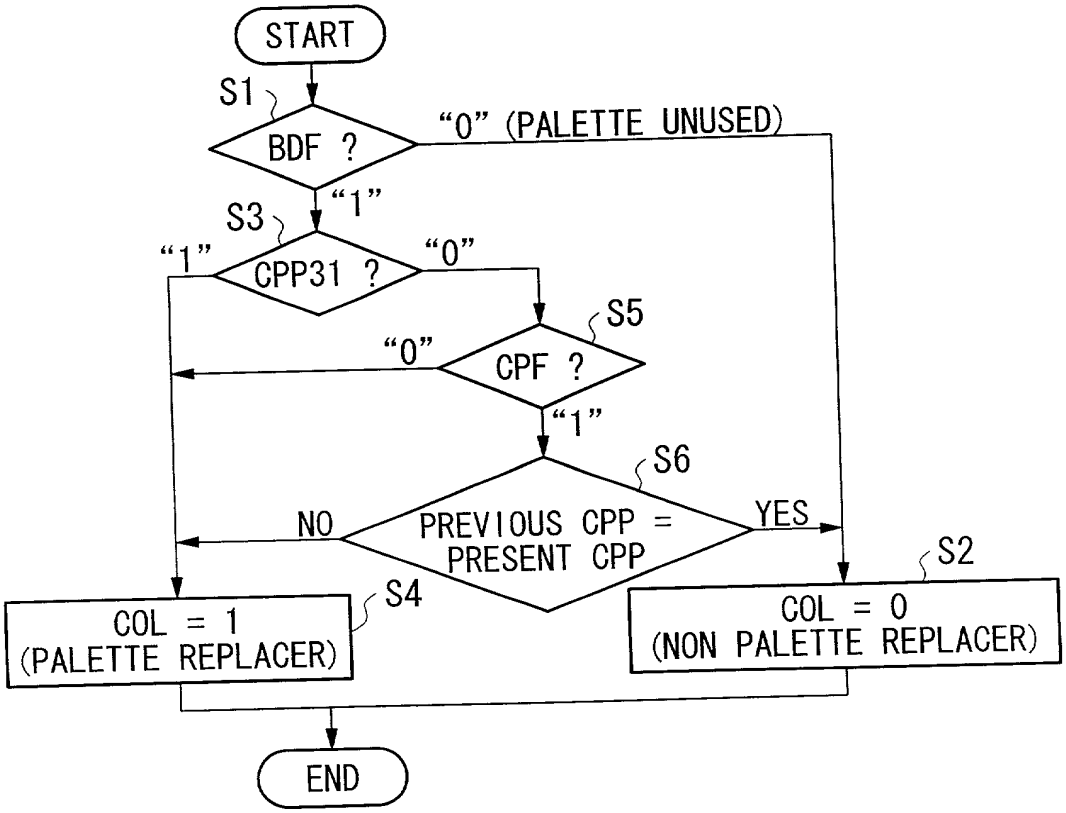

21
BDF
COL
22
CPP31
33
27
30
32
23
28
CPF
24
31
PREVIOUS CPP
25
COMPARISON CIRCUIT
PRESENT CPP
NO MATCH→ "1"
29

FIG. 10


START
S1
BDF ?
"0" (PALETTE UNUSED)
"1"
S3
"1"
CPP31 ?
"0"
S5
"0"
CPF ?
"1"
S6
NO
PREVIOUS CPP = PRESENT CPP
YES
S2
S4
COL = 1 (PALETTE REPLACER)
COL = 0 (NON PALETTE REPLACER)
END